

Enhancing Power Quality under Converter Transient Non -Liner Loads in VSG-Driver Micro Grid ANFIS Control Strategy

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ABSTRACT: Islanded microgrids controlled by Virtual Synchronous Generators (VSGs) frequently suffer from poor power quality and voltage distortion when supplying unpredictable nonlinear loads or pulse loads. This paper presents an advanced control strategy that combines an Adaptive Neuro-Fuzzy Inference System (ANFIS) with a quasi-harmonic voltage feedforward loop to enhance microgrid stability and power quality. Bypassing the limitations of classical fixed-parameter swing equations, the ANFIS controller dynamically learns from power errors to actively infer the optimal angular frequency for the inverter. Simultaneously, the feedforward loop actively cancels out quasi-harmonic voltage distortions caused by nonlinear currents. The efficacy of the proposed model is validated through MATLAB/Simulink simulations under highly dynamic transient conditions, including the sudden connection of rectifier

and pulse loads. The results confirm that the proposed ANFIS controller efficiently regulates power flow and completely suppresses load-induced harmonics, delivering a steady, high-quality AC power supply to the islanded microgrid..

KEYWORDS: MICROGRID, VSG, POWER QUALITY, HARMONICS.

INTRODUCTION: WITH the development of renewable electricity generations, microgrids are increasing swiftly [1]. Microgrid can perform in grid-related [2] and islanded mode [3]. Since the islanded microgrid lacks voltage help from the general public grid, the voltage stability of the islanded microgrid is terrible. By simulating the voltage and frequency traits of a synchronous generator, digital synchronous generator (VSG) control is proposed, that could offer inertia and damping for the islanded microgrid [4]. Presently, the VSG-primarily based islanded microgrid has received widespread studies

because of its outstanding control traits [5], [6], [7]. However, because of the small capability, the VSG-based totally islanded microgrid is liable to cause voltage harmonics underneath nonlinear load disturbance, which requires extra interest [8]. Currently, the harmonic suppression schemes of islanded microgrids are usually divided into two classes: one is to feature the specialized harmonic suppression device, and the opposite is to optimize the unique manipulate algorithm. The widely used harmonic suppression devices include passive power filters [9], active strength filters [10], [11], [12], and many others. Compared to the specialized tool, it's far extra inexpensive to suppress harmonic via optimizing the manipulate algorithm.

Thus, scholars have performed massive studies on manage algorithm optimization for suppressing harmonics [7-10]. To minimize the harmonic distortion of the islanded microgrid on line, an wise controller primarily based at the particle swarm optimization algorithm is proposed. Since the voltage reference of each dispensed generator is based at the primary controller to iteratively correct, the designed controller calls for a substantial computational workload and has a lengthy dynamic process. To reduce the total

harmonic distortion (THD) of the microgrid below rectifier load disturbance, a present day controller based totally on repetitive manipulate and H_∞ is proposed. Because the present day manipulate signal desires to be corrected by using the previous manipulate, the dynamic performance of the designed manipulate is fantastically terrible. As is widely known, the harmonic modern as a result of the nonlinear load will bring about a harmonic voltage on the inverter impedance.

Therefore, by means of reducing the inverter impedance, the voltage harmonics of the islanded microgrid can be successfully suppressed. In digital impedance manipulate of the islanded microgrid is proposed, which suppresses voltage harmonics by using reducing the inverter impedance at the harmonic frequency factor. As the voltage harmonics need to be extracted one by one, the designed harmonic suppression method is very complex and may most effective suppress the harmonics extracted via the clear out. In a a couple of harmonic collection issue observer (MHSCO) is proposed, that may mitigate the regarded harmonics for the reason that designed MHSCO can best extract the harmonics at a fixed frequency. With the improvement of

strength electronics generation, the load sorts of the islanded microgrid grow to be numerous, which include the heart beat load. Due to the time-varying characteristics, the voltage harmonics as a result of the heartbeat load are usually unknown earlier. The existing harmonic suppression strategies are especially designed for integer harmonics, together with 5th, 7th, and eleventh, that may hardly ever suppress the uncertain inter-harmonics caused by way of the heartbeat load. Thus, it needs to research greater superior harmonic suppression algorithms.

In addition, the widely used theoretical evaluation methods of energy exceptional are the kingdom-space approach and the impedance technique. The nation-area version of the islanded microgrid turns into complex as the gadget scale grows, that may motive a dimensional catastrophe. Meanwhile, the nation-area technique analyzes the islanded microgrid from a mathematical angle and lacks practical physical significance. Compared with the nation-area method, the impedance technique can version every a part of the islanded microgrid one by one, that is notably simple [3]. Moreover, thanks to the clear bodily which means, the impedance

version of the islanded microgrid can provide theoretical steerage for designing the harmonic suppression method. Based at the above analysis, the impedance approach is a good choice for reading the energy nice of the islanded microgrid.

II.LITARATURE SURVEY:

1) Synchronverters: Inverters that mimic synchronous generators

In this paper, the idea of operating an inverter to mimic a synchronous generator (SG) is motivated and developed. We call the inverters that are operated in this way synchronverters. Using synchronverters, the well-established theory/algorithms used to control SGs can still be used in power systems where a significant proportion of the generating capacity is inverter-based. We describe the dynamics, implementation, and operation of synchronverters. The real and reactive power delivered by synchronverters connected in parallel and operated as generators can be automatically shared using the well-known frequency- and voltage-drooping mechanisms. Synchronverters can be easily operated also in island mode, and hence, they provide an ideal solution for microgrids or smart grids. Both simulation and experimental results are given to verify the idea.

2) A DC Hybrid Active Power Filter and Its Nonlinear Unified Controller Using Feedback Linearization

In current power system, the conversion between DC and AC is widely existing and dc side harmonic problem is prominent. To suppress the second-harmonic current (SHC) at the dc side of single-stage single-phase inverter, a dc hybrid active power filter (DC-HAPF) structure is presented, which composes of bidirectional dc-dc circuit based active power filter and CL passive filter. Here, the CL passive filter is used to mitigate the high frequency harmonics and the active power filter is applied to compensate the low frequency harmonic current. Meanwhile, the influence of filter parameters on harmonic suppression is analyzed based on the average switching model. In addition, for the control of the DC-HAPF, a nonlinear unified controller via feedback linearization is proposed, where the voltage and current dual-loop control is converted to a single loop control of energy. By analyzing the control system stability and DC-HAPF's performance, appropriate control parameters are selected. To verify the feasibility of the proposed topology and control strategy, a 500W single-stage single-phase inverter with the DC-HAPF is built

and a good performance of dc side harmonic suppression has been achieved.

III.SYSTEM CONFIGARATION: In this paper a quasi-harmonic voltage feed forward control is proposed to improve the power quality of the VSG-based islanded microgrid. The main work and contribution are given as follows:

- 1) To suppress the voltage harmonics of the VSG-based islanded microgrid under load disturbances, a quasi harmonic voltage feedforward control is proposed based on the rated voltage and sampling voltage, which does not need harmonic-extraction filter and can suppress wideband harmonics without knowing the harmonic frequency in advance.
- 2) The parameter design principle of the proposed control is introduced, and the reason why the proposed control can automatically suppress the wideband harmonics is revealed from the impedance perspective.
- 3) The small-signal model of the improved VSG is built, and the stability of VSG before and after improvement is compared theoretically. Moreover, the proposed control and theoretical analysis are validated by simulation results.

Fig. 1 shows the topology and control diagram of the VSG-based islanded microgrid, where V_{dc} represents the dc-side voltage; L_f , C_f , and R_d represent the filter inductor, filter capacitor, and damping resistor, respectively; R_l , L_l , and C_l represent the resistor, inductor, and capacitor of the linear load, respectively; L_n and C_n represent the inductor and capacitor of the nonlinear load, respectively; R_n and R_p represent the rectifier resistor and pulse resistor, respectively; f_{pu} represents the pulse frequency; i_{abc} and v_{abc} represent the inductor current and microgrid voltage, respectively; e_{abc} represents the inner electric potential of VSG; and $Z_{in}(s)$ represents the impedance of VSG. From Fig. 1, the active and reactive power controllers can be expressed as follows:

$$\begin{cases} (\omega_n - \omega_m) D_p + P_{ref}/\omega_n - P_m/\omega_n = J s \omega_m \\ \sqrt{2} (V_n - V_m) D_q + Q_{ref} - Q_m = \sqrt{2} E_m K s \end{cases} \quad (1)$$

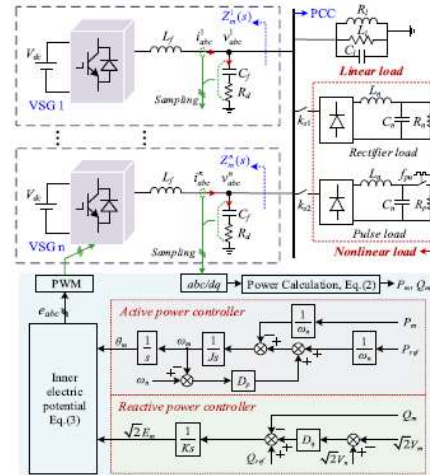


Fig. 1. Topology and control diagram of the VSG-based islanded microgrid.

where P_{ref} and Q_{ref} represent the rated values of the active and reactive power, respectively; V_m and V_n represent the root mean square (rms) value and rated rms value of the microgrid voltage, respectively; θ_m and E_m represent the phase angle and rms value of e_{abc} , respectively; D_p and D_q represent the active and reactive droop factors, respectively; ω_m represents the angular frequency, which can be expressed as $\omega_m = s\theta_m$; ω_n represents the rated angular frequency; J represents the virtual inertia; K represents the damping coefficient; and P_m and Q_m represent the instantaneous active and reactive power respectively, which can be calculated as follows:

$$\begin{cases} P_m = 1.5(v_\alpha i_\alpha + v_\beta i_\beta) \\ Q_m = 1.5(v_\beta i_\alpha - v_\alpha i_\beta) \end{cases} \quad (2)$$

Where v_α and v_β represent the microgrid voltage in $\alpha\beta$ domain, respectively; i_α and i_β represent the inductor current in $\alpha\beta$ domain, respectively.

In Fig. 1, the inner electric potential eabc of VSG can be calculated as follows:

$$\begin{cases} e_a = \sqrt{2}E_m \cos \theta_m \\ e_b = \sqrt{2}E_m \cos(\theta_m - 2\pi/3) \\ e_c = \sqrt{2}E_m \cos(\theta_m + 2\pi/3). \end{cases} \quad (3)$$

According to [27], the sequence impedance of VSG can be expressed as follows:

$$\begin{cases} Z_{sp}(s) = \frac{0.75V_1M(s-j2\pi f_1)K_{pwm}K_1(s)e^{j\theta_d}/\omega_n + sL_f}{1+0.75I_1M(s-j2\pi f_1)K_{pwm}K_1(s)e^{j(\theta_d-\theta_{i1})}/\omega_n} \\ Z_{sn}(s) = \frac{0.75V_1M(s+j2\pi f_1)K_{pwm}K_1(s)e^{-j\theta_d}/\omega_n + sL_f}{1+0.75I_1M(s+j2\pi f_1)K_{pwm}K_1(s)e^{-j(\theta_d-\theta_{i1})}/\omega_n} \end{cases} \quad (4)$$

where $Z_{sp}(s)$ and $Z_{sn}(s)$ represent the positive- and negative-sequence impedances without considering the filter capacitor and damping resistor, respectively; V_1 , I_1 , and f_1 represent the fundamental voltage amplitude, the fundamental current amplitude, and the fundamental frequency, respectively; θ_{i1} represents the initial phase angle of I_1 ; $\theta_d = \arcsin[P_m\omega_n L_f/(E_m V_1)] + \pi/2$, $\arcsin[P_m\omega_n L_f/(E_m V_1)]$ represents the power angle of VSG; $K_{pwm} \approx (1 + 1.5T_s)$, and T_s represents the switching period. $K_1(s) = \sqrt{2}E_m/[1 + s/\omega_v)(1 + s/\omega_i)]$, where ω_i and ω_v represent the cutoff frequencies of current and voltage sampling filters, respectively, $M(s) = 1/(Js^2 + Dps)$. Considering the filter capacitor C_f and damping resistor R_d , the equivalent

impedance of VSG can be expressed as follows:

$$\begin{cases} Z_{inp}(s) = Z_{sp}(s) \parallel \left(R_d + \frac{1}{sC_f}\right) \\ Z_{inn}(s) = Z_{sn}(s) \parallel \left(R_d + \frac{1}{sC_f}\right) \end{cases} \quad (5)$$

Where $Z_{inp}(s)$ and $Z_{inn}(s)$ represent the positive- and negative-sequence impedance of VSG, respectively. Fig. 2 shows the frequency response of the impedance $Z_{inp}(s)$ and $Z_{inn}(s)$. As seen in Fig. 2, when the frequency is above 100 Hz, the impedance response curves of $Z_{inp}(s)$ and $Z_{inn}(s)$ are basically the same. Since this article focuses on the impedance above 100 Hz, $Z_{inp}(s)$ and $Z_{inn}(s)$ are uniformly represented by $Z_{in}(s)$ in this article. Fig. 3 depicts the equivalent circuit of the studied islanded microgrid. In Fig. 3, $\tilde{v}_{pcc}$ represents the microgrid voltage; $\tilde{i}_f$ and $\tilde{i}_h$ represent the fundamental current and harmonic current of load, respectively. From Fig. 3, the harmonic voltage $\tilde{v}_{pcch}$ can be expressed as follows:

$$\tilde{v}_{pcch} = \tilde{i}_h [Z_{in}^1(s) \parallel \cdots \parallel Z_{in}^n(s)] = \tilde{i}_h \frac{1}{\sum_{l=1}^n \frac{1}{Z_{in}^l(s)}}. \quad (6)$$

From (6), the harmonic current $\tilde{i}_h$ induced by the load can cause a harmonic voltage $\tilde{v}_{pcch}$ on the equivalent impedance of the islanded microgrid. The harmonic frequency

of $\sim \text{vpch}$ is related to load. In the case of a typical rectifier load, voltage harmonics are mainly integer harmonics, such as 5th and 7th.

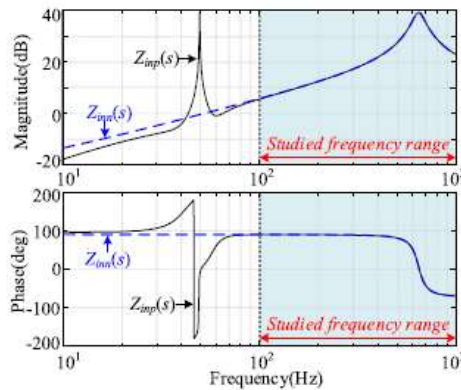


Fig. 2. Frequency response of the impedance $Z_{in}(s)$ and $Z_{inn}(s)$.

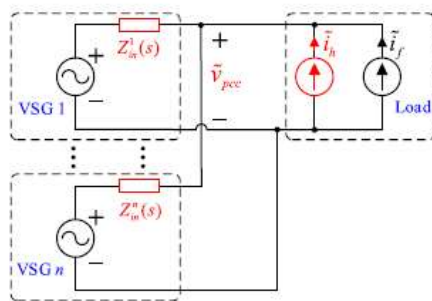


Fig. 3. Equivalent circuit of the studied islanded microgrid.

TABLE I
SIMULATION PARAMETERS

Parameter	Value	Parameter	Value	Parameter	Value
V_{dc}/V	700	D_q	321	L_l/mH	5
V_n/V	220	J	0.057	C_l/uF	300
P_{ref}/kW	10	K	7.1	R_n/Ω	40
$Q_{ref}/kVar$	0	L_f/mH	3	L_n/mH	1
f_i/Hz	50	C_f/uF	20	C_n/uF	10
$f_c(1/T_d)/kHz$	20	R_d/Ω	1.7	R_p/Ω	50
D_p	5	R_l/Ω	10	f_{pu}/Hz	180

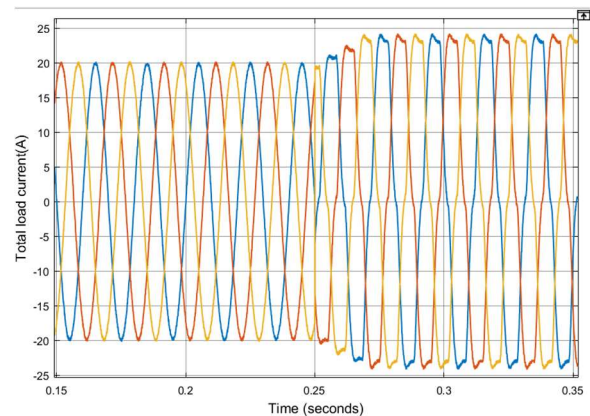
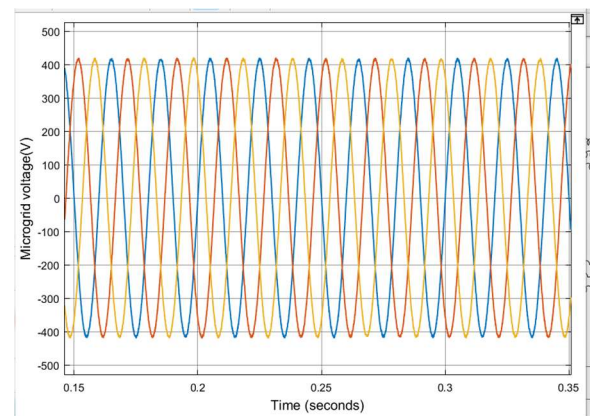
Under the pulse load disturbance, the harmonic frequency caused by the pulse load changes with the pulse frequency, which is usually time-varying and unknown.

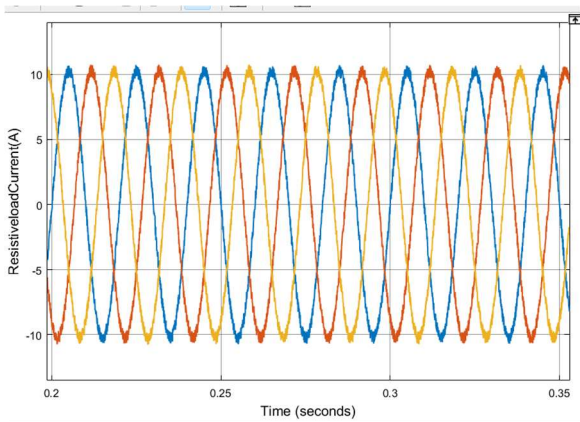
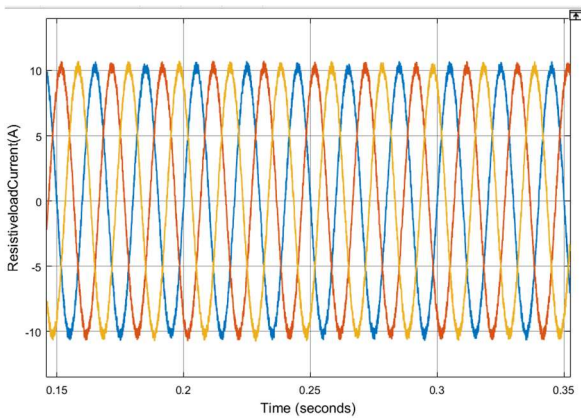
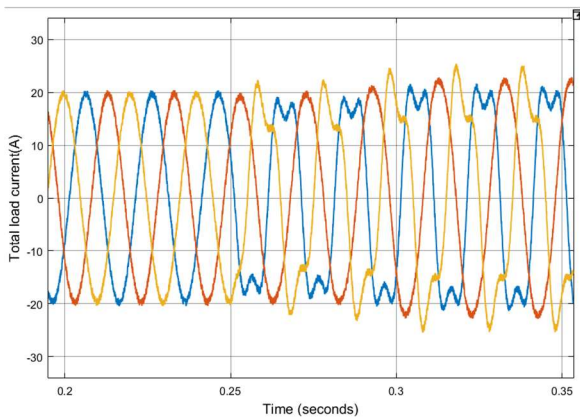
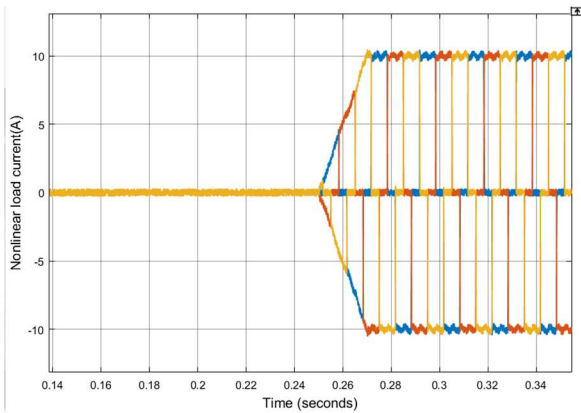
To verify the theoretical analysis above, an islanded microgrid controlled by VSG, as shown in Fig. 1, is simulated in MATLAB/Simulink. Simulation parameters are listed in Table I.

IV.SIMULATION RESULTS:

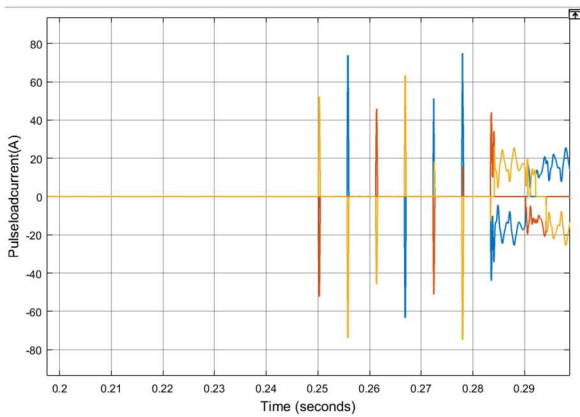
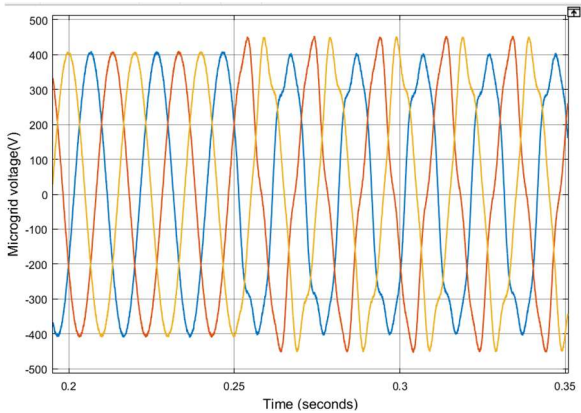
Conventional method

Case1



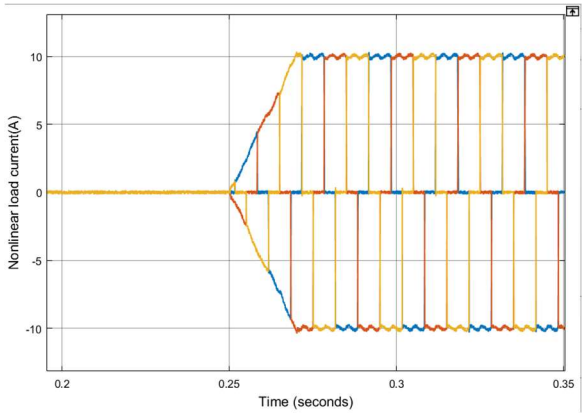
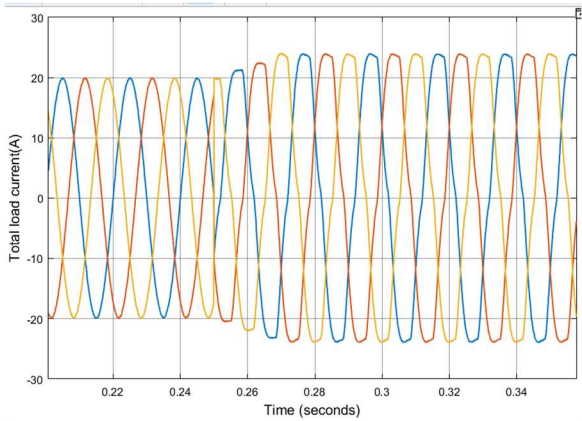
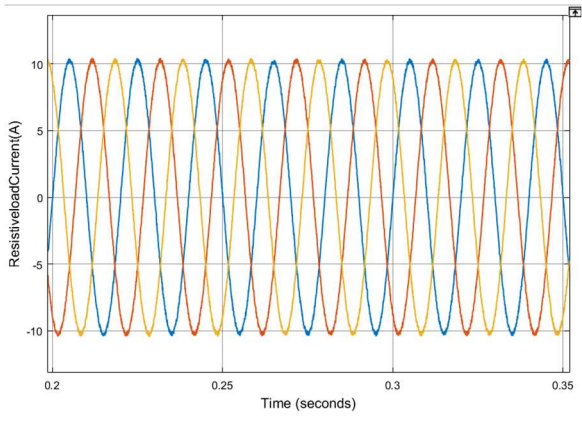
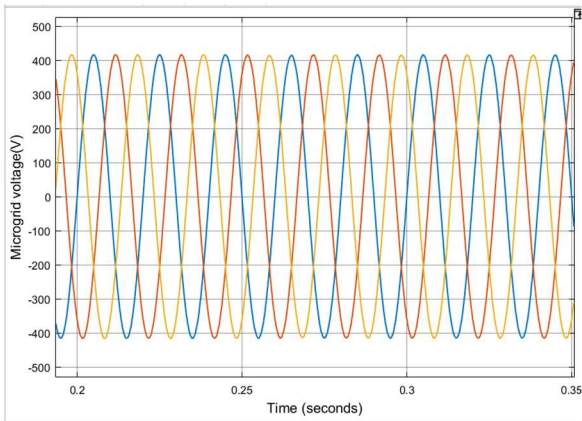


Case2

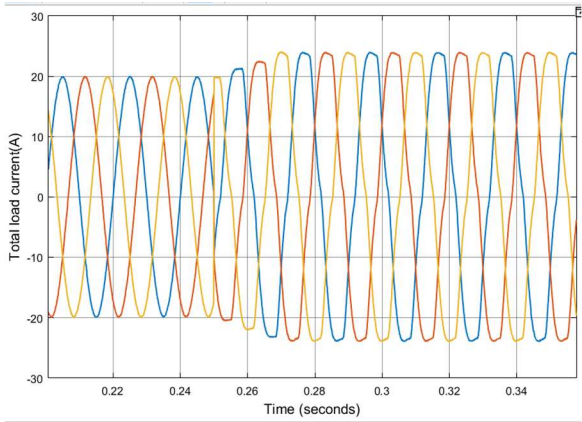
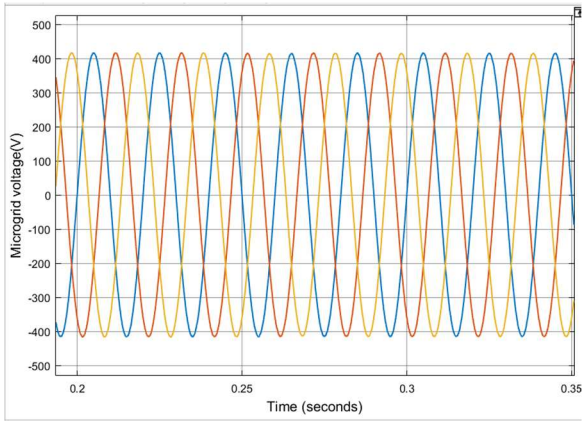


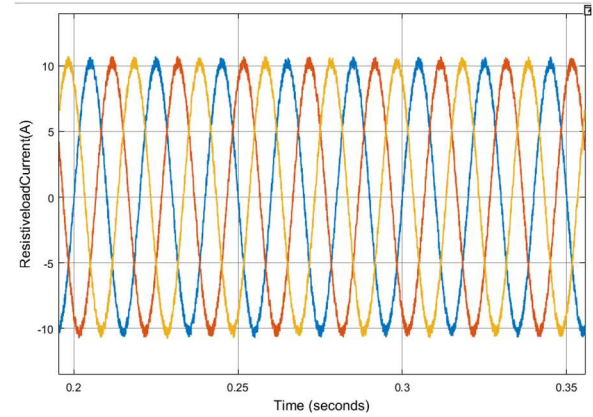
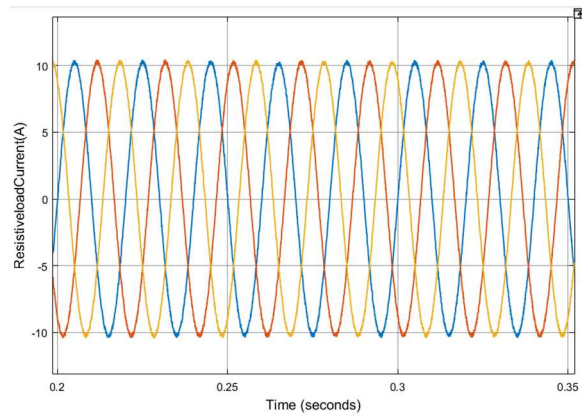
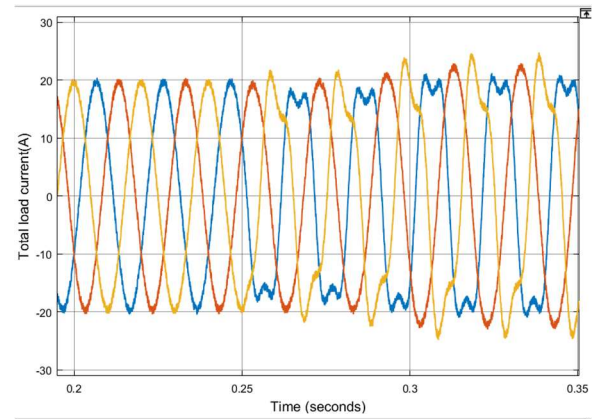
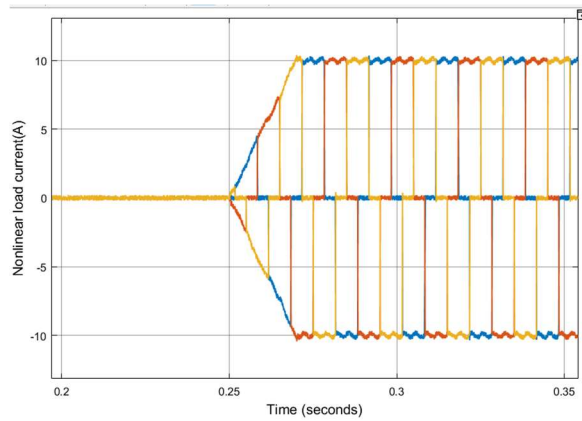
Proposed method

Case1

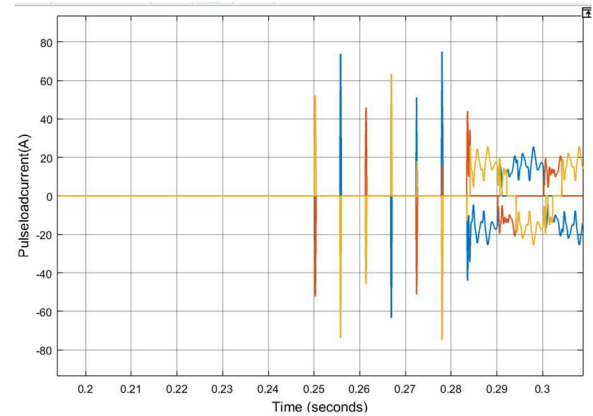
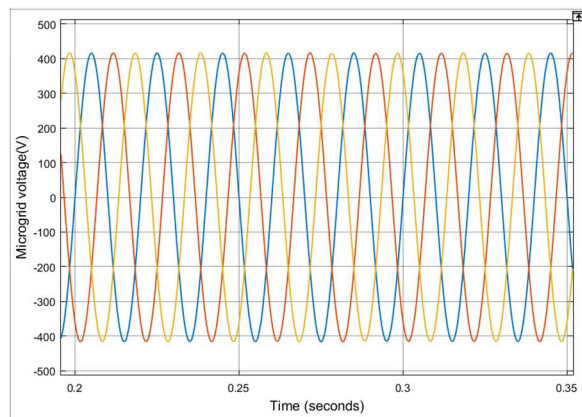


Case2





Case3



V.CONCLUSION: In this study, a novel ANFIS controller combined with a quasi-harmonic voltage feedforward technique was proposed to improve the stability and power quality of VSG-based islanded microgrids. Unlike traditional VSG controls

that experience severe voltage distortion under nonlinear loads due to high virtual impedance, the proposed method actively and dynamically regulates the inverter output to maintain power quality. By replacing fixed-parameter mathematical models with an ANFIS controller, the system intelligently maps power errors to appropriate angular frequency adjustments, allowing for a superior, fast-acting dynamic response to severe transient disturbances. The inclusion of the quasi-harmonic feedforward loop significantly reduces the equivalent impedance of the VSG. This allows the system to automatically isolate and suppress wideband voltage harmonics. From the Simulation outcomes it is observed that under both sudden rectifier load and high-frequency pulse load disturbances confirmed that the proposed control scheme regulates power flow effectively and reduced reduces THD significantly.

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